Comp 790-184: Hardware Security and Side-Channels

Introduction

January 19, 2024 Andrew Kwong

Department of Computer Science



THE UNIVERSITY of NORTH CAROLINA at CHAPEL HILL

Slides adapted from Mengjia Yan (shd.mit.edu)

Today's Class

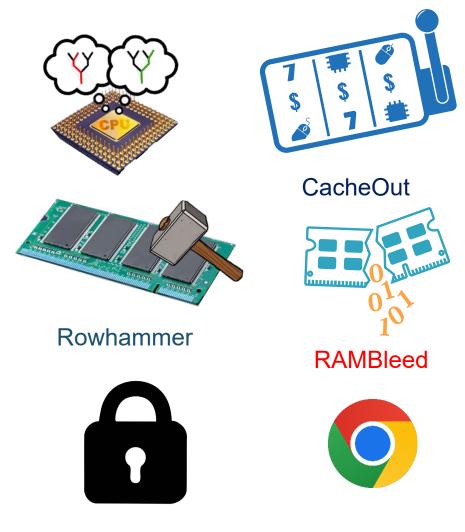
- Introductions
- Course Goals
- Course Structure
- Intro to Side-Channels





Who am I?

- Andrew Kwong
 - Assistant Professor
- Site: https://andrewkwong.org
- Email: andrew@cs.unc.edu
- Office: FB 340
- Office Hours: Thursday 2:00-3:00



My Research

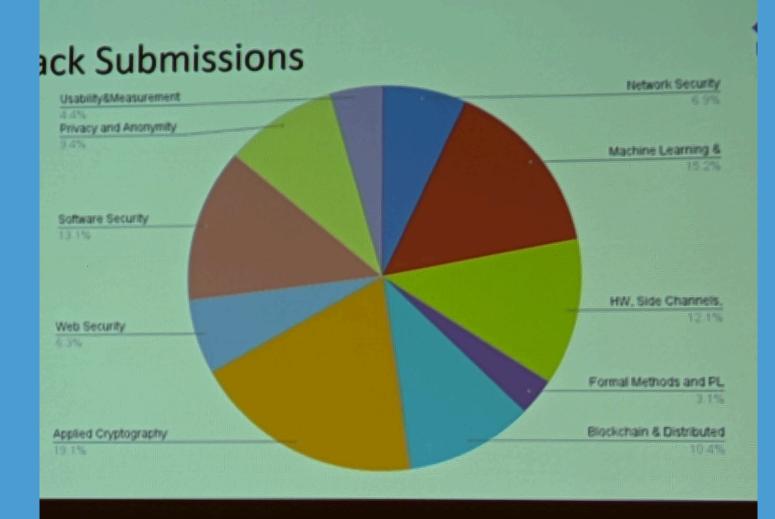
- Side-Channels:
 - Memory
 - CPU
 - Applied-crypto

Who are you?

- Research Field
- Hobby/interesting fact
- Something you want to learn from this course.

Course Goals

- Get hands-on experience with side-channel attacks
 - Develop real-world attacks against real hardware
 - Learn how to defend against these attacks
 - Build toolkit for side-channel/hardware security research
- Gain high-level understand of the science
 - Discover where this newish field is going
 - Find out what problems are interesting





Course Structure

Structure

- Course meetings split 50-50 between lectures and paper discussions
- Lab Assignments
 - Programming based assignments leading towards real-world attacks on actual hardware
 - Putting theory into practice

Grading

- Class Participation 10%
- Paper Presentations 15%
- Lab Assignments–75%

Class Participation (10%)

- Ask insightful questions
- For paper readings:
 - Come prepared to contribute to paper discussions
 - Write down two strengths and two weaknesses of the paper
 - Write down at least one insightful question (you may be asked to share!)

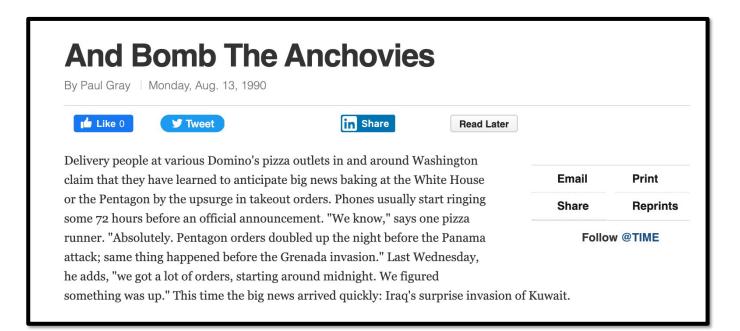
Paper Presentations (15%)

- Give conference style talk on assigned papers
- Can make or reuse/augment slides
- Roughly 20 minute presentation
 - High level advertisement for the paper
 - Impart the most important information
- Prepare discussion questions for the class

Lab Assignments(75%)

- 3-4 "CTF-style" labs
 - cache side-channels
 - Spectre attacks
 - Rowhammer
 - Speculative attack for ASLR break
- Discussing with classmates is allowed
 - Must write your own code

What are Side-Channels?



By making indirect observations (the number of pizzas ordered), one is able to infer partial information

Safe Cracking

 Should be secure, given enough combinations



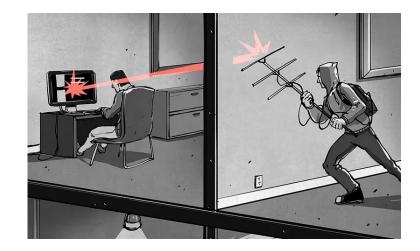
Imperfections in the implementation indirectly leak information

Covert Channels vs Side Channels

- Gather information by measuring or exploiting **indirect** effects of the system or its hardware -- rather than targeting the program or its code directly.
- Covert channel:
 - Cooperated/Intended communication between two or more security parties
 - Sender and receiver are cooperating
- Side channel:
 - Unintended communication between two or more security parties
 - Receiver is not cooperating
- In both cases:
 - Communication should not be possible, following system semantics
 - The communication medium is not designed to be a communication channel
 - Imperfection in the *implementation* leaks information

EM Side-Channels

- Tempest paper written in 1972 (top secret)
- Standards for shielding sensitive equipment
 - Monitor contents can be recovered from EMR
- Researchers have demonstrated:
 - Stealing all kinds of cryptographic keys
 - fingerprinting



Acoustic Side Channels

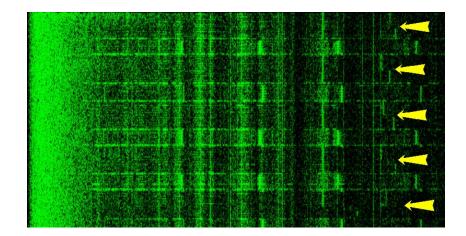
- Monitor keystroke
 - You only need: a cheap microphone + an ML model
- Other sources of acoustic side channels inside a computer?



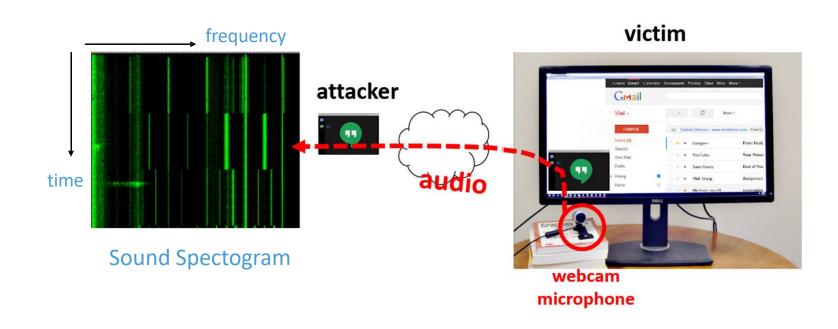


Acoustic Cryptanalysis

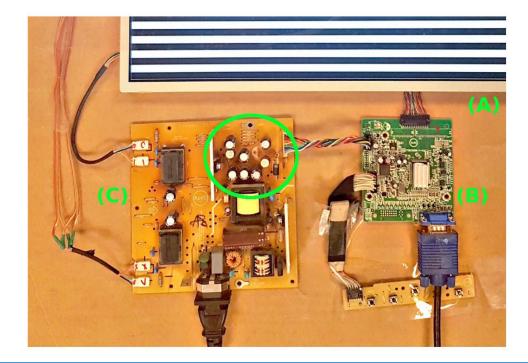
- Ceramic capacitors also
 leak
- Different operations on the CPU create different sounds
- Can extract RSA key from GPG!



"Hear" The Screen



"Hear" The Screen



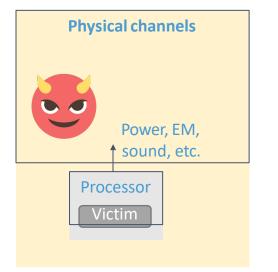
(A) is the LCD panel, (B) is the screen's digital logic and image rendering board and, (C) is the screen's power supply board.

Example 3: Timing Side Channel

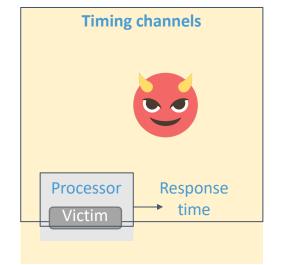
```
def check_password(input):
 size = len(password); # 128 ASCII
 for i in range(0,size):
     if (input [i] == password[i]):
         return ("error");
 return ("success");
```

- How many attempts the attacker needs to crack the password?
- Can we reduce the number of attempts? How?
- Numerous timing sidechannels have also been demonstrated against cryptographic algorithms

A Rough Classification based on What Attackers Can Observe

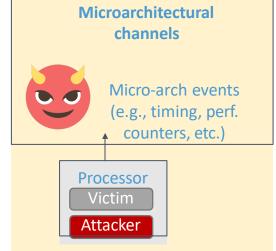


Attacker requires measurement equipment → physical access



Attacker may be remote (e.g., over an internet connection)

Requires shared hardware



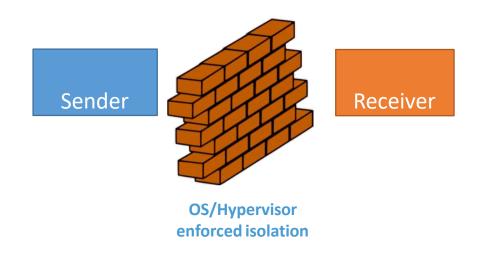
Attacker may be remote, or be co-located

Where is hardware shared?



Virtual Machine	Virtual Machine	Virtual Machine
Арр А	Арр В	Арр С
Guest Operating System	Guest Operating System	Guest Operating System
	Hypervisor	
	Infrastructure	

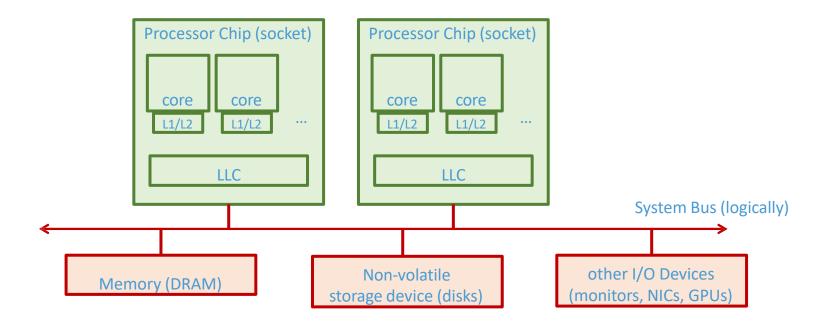
Threat Model



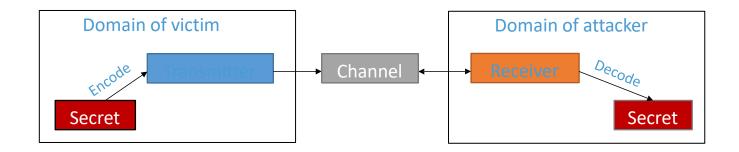


File, Socket, Pipe, Shared memory (shm in Linux) ...

uArch Attacks Generalization



A Communication Model



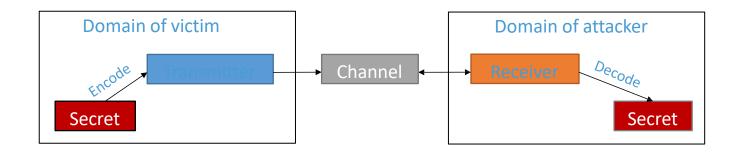
Communication Protocols

- How to encode?
 - Encode secrets via time or space
- How to coordinate between the sender and receiver?
 - Synchronization
- Bandwidth

RDRAND unit: 7-200 Kbps MemBus/AES-NI contention: ~550-650 Kbps LLC: 1.2 Mbps Various structures on GPGPU: up to 4 Mbps

(Data from research papers. Not fully optimized)

Mitigations



- Sender does not use the channel -> "data-oblivious execution" or "constant-time programming". (more in a later lecture)
- Making disjoint channels makes communication impossible.
- Add noise.

To be continued...

Your Assignments

- First paper discussion next Thursday
 - Write down two strengths and two weaknesses of the paper
 - Write down at least one insightful question (you may be asked to share!)
- Rate preferences for paper presentations (let me know if you have a preference for presenting twice for extra credit)



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